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UTILITY PATENT APPLICATION TRANSMITTAL		Attorney Docket No. GK-US035197	
(Only for new nonprovisional applications under 37 C.F.R. 1.53(b))		First Inventor Min-Su Jeong et al.	
		Title PHASE LOCKED LOOP FREQUENCY SYNTHESIZER WHERE FREQUENCY GAIN VARIATION OF VOLTAGE CONTROLLED OSCILLATOR IS COMPENSATED	
		Express Mail Label No.	
APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents.		ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231	
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FEE TRANSMITTAL FORM

Attorney Docket No.: **GK-US035197**

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Transmitted herewith for filing is the patent application of

Inventor: **Min-Su JEONG et al.**

For: **PHASE LOCKED LOOP FREQUENCY SYNTHESIZER WHERE FREQUENCY GAIN
VARIATION OF VOLTAGE CONTROLLED OSCILATOR IS COMPENSATED**

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Dated: 12/1/03


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